PVP 12

Code: CS4T5

II B.Tech - II Semester – Regular Examinations - JUNE 2014

MICRO PROCESSORS & INTERFACING
(COMPUTER SCIENCE & ENGINEERING)

Duration: 3 hours Marks: 5x14=70

Answer any FIVE questions. All questions carry equal marks

1. a) Explain 8086 flag Register with the help of a diagram. 7 M

   b) Explain about 8086 addressing modes with example. 7 M

2. Write an Assembly Language Program to check for Palindrome String. Write about each step in brief. 14 M

3. a) Explain the bus timing of memory write operation in the maximum mode configuration of the microprocessor 8086. 7 M

   b) Explain the bus timing of memory read operation in the maximum mode configuration of the microprocessor 8086. 7 M

4. a) Draw and explain the block diagram of the 8255 PPI. 7 M

   b) Describe the mode 0, mode 1 and mode 2 of in the input- output operation of 8255 PPI. 7 M

5. a) Describe the action taken by 8086 when: 7 M

(i) NMI pin is activated.

(ii) INTR pin is activated.

b) Draw and explain the interrupt acknowledge cycle of 8086. 7 M

6. a) Draw and explain the status word format for 8251. 4 M

b) Explain various programmable features of 8251. 10 M

7. a) Describe about the two addressing modes that 80286 CPU operates in. 3 M

b) Explain registers in 80286. 5 M

c) What is Privilege? What is meant by task privilege? 6 M

8. a) Explain about Pentium processor registers. 7 M

b) Write a note on Real and Protected modes in Pentium processors. 7 M